

V<sub>bb</sub> SUBSTRATE BIAS (VOLTS)

FIG. 1

App No.: 09/652,216

Docket No.: M4065.0227/P227

Inventor: Tongbi Jiang et al.

**Title: METHOD AND APPARATUS FOR REDUCING  
SUBSTRATE BIAS VOLTAGE DROP**

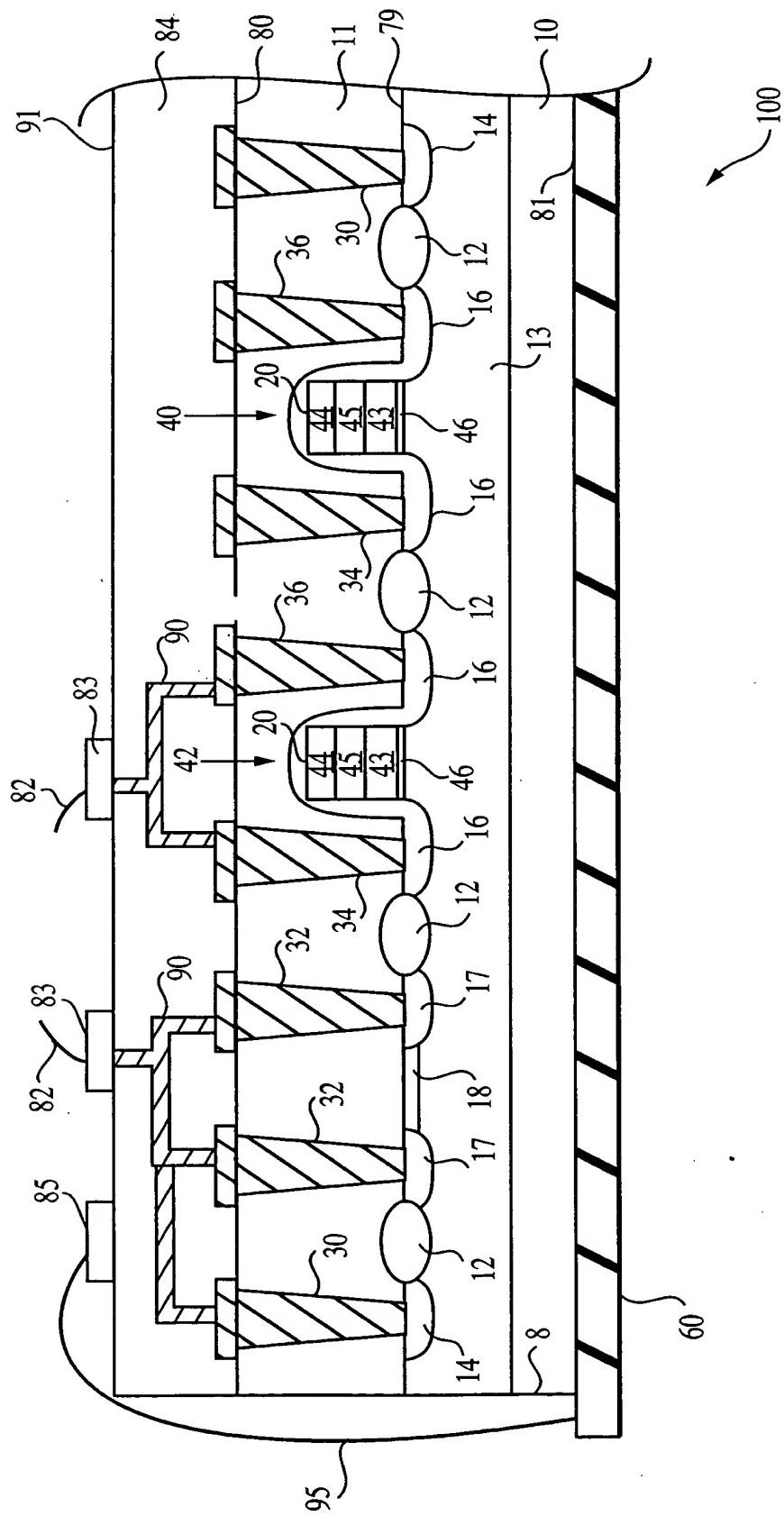


FIG. 2

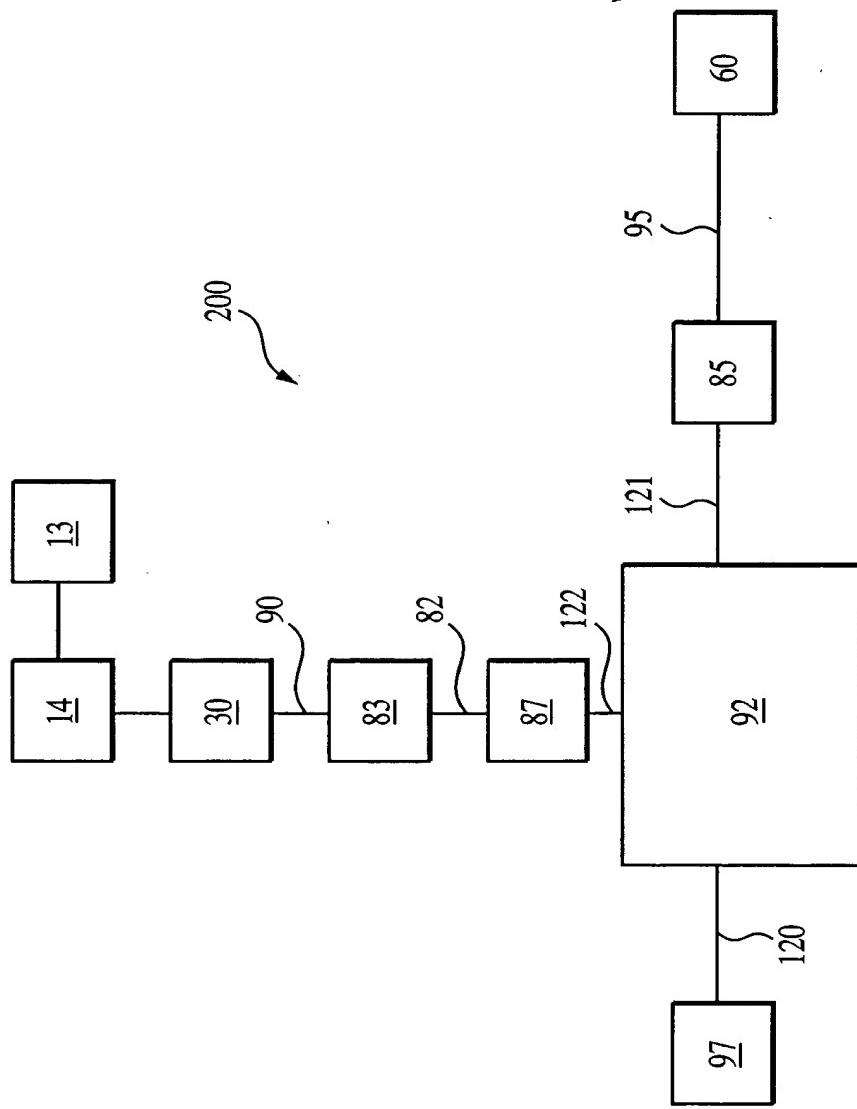


FIG. 3

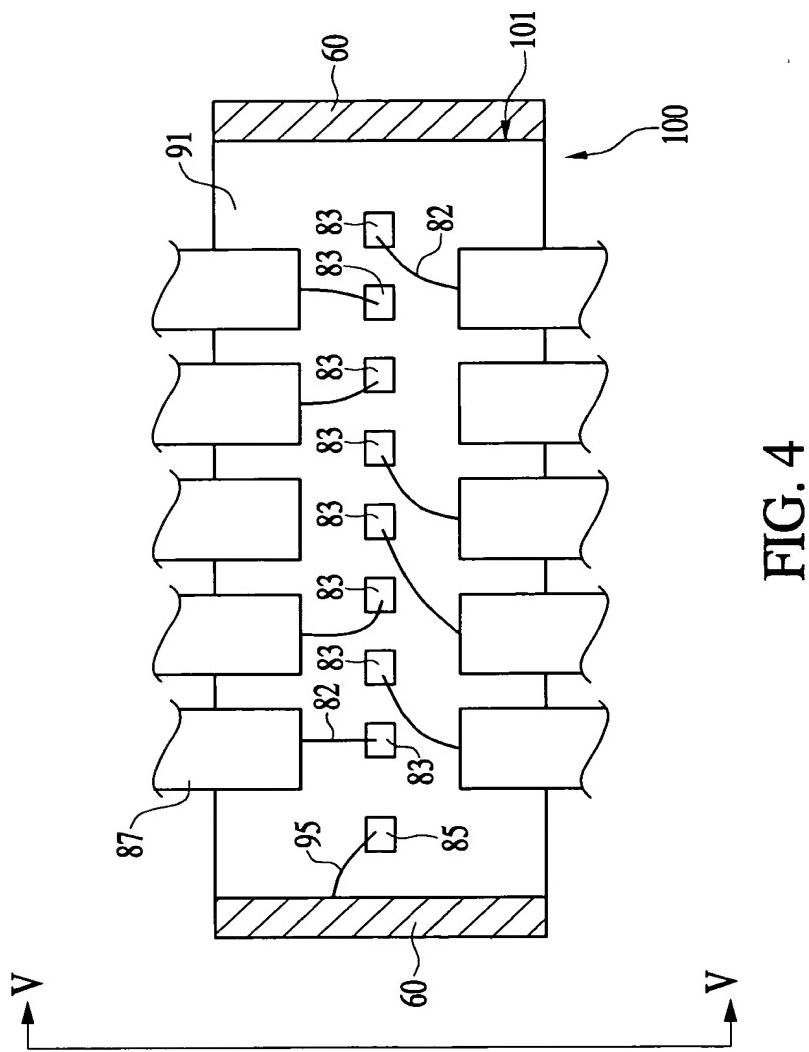


FIG. 4

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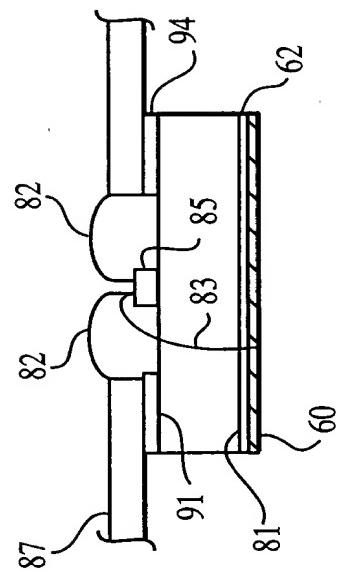
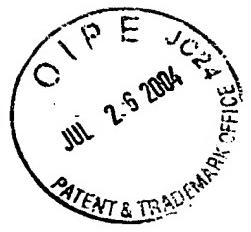


FIG. 5

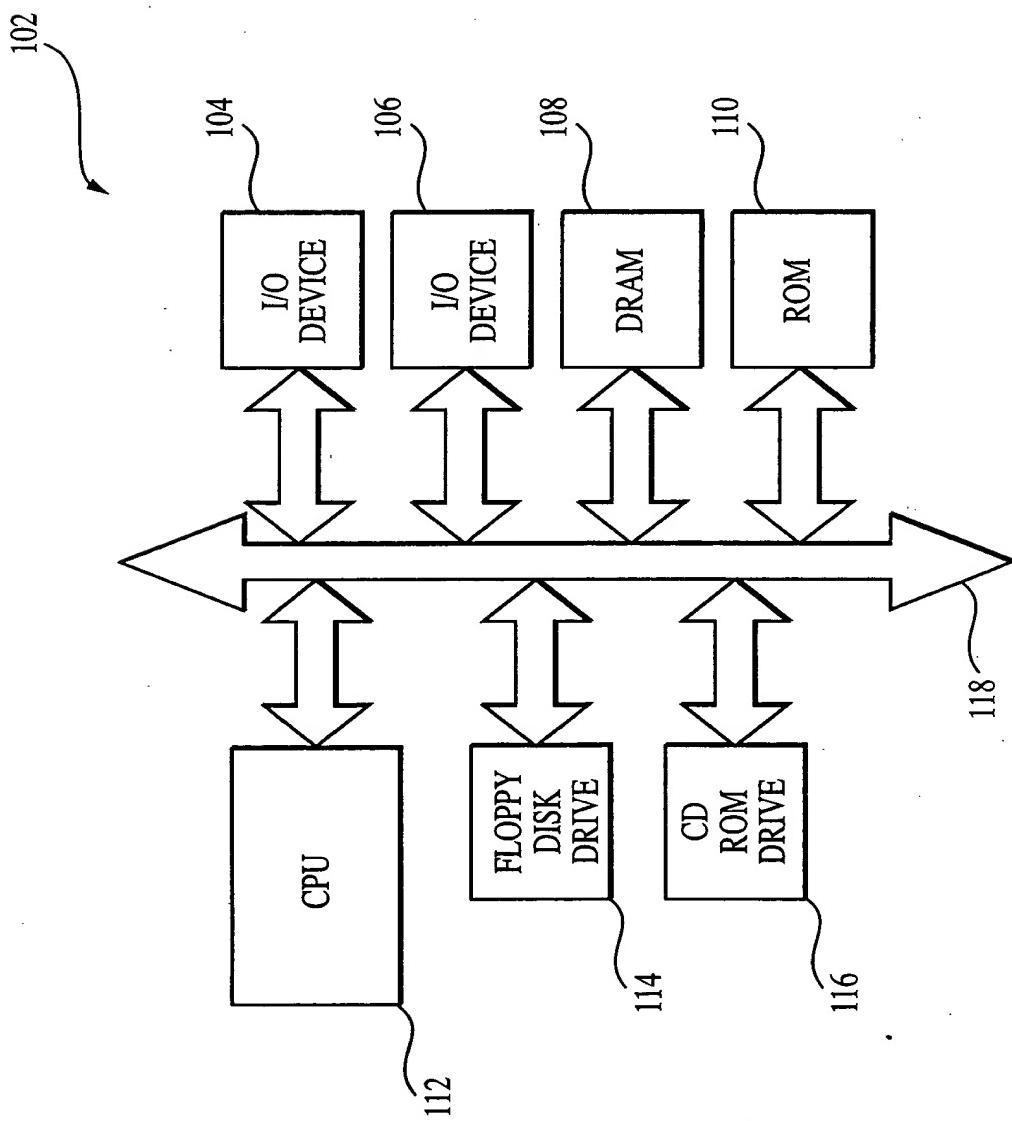


FIG. 6